

AMENDMENTS TO THE SPECIFICATION

Please amend the paragraph beginning on line 11 of page 20 of the specification as follows:

Figure 6 is a pictorial diagram illustrating the destination node ID and destination address translation from a local PCI bus address in accordance with one embodiment of the present invention. In the embodiment shown, local PCI address 605 is a 64 bit word that includes base address 610, logical node ID 615 and address offset 620. Logical node ID 615 is translated into destination node ID 625. Base address 610 and address offset 620 are translated into destination address 630. Destination address 630 is a 64 bit word in this embodiment. ~~The size of destination node ID 625 as well?~~ Destination node ID 625 and destination address 630 can then be provided to a read/write transfer unit (e.g., 313 of Figure 3).